**ASSIGNMENT 3**

1. With a neat diagram, explain internal organization of 16\*8 memory chip.
2. With a neat diagram, explain internal organization of a 2M\*8 dynamic memory chip.
3. With a neat diagram explain a single transistor dynamic memory cell.
4. Briefly explain the working of 1 bit CMOS SRAM cell with a schematic.
5. With a neat diagram, explain the working principle of magnetic disk.
6. With a neat diagram, explain virtual memory organization.
7. Explain with a neat diagram, Single bus organisation of data path inside a processor.
8. Explain multiple bus/three bus organization, with a neat diagram**.**
9. Explain hardwired Control Unit Organisation
10. Describe basic organization of a microprogrammed control unit. Give an example of microinstructions.
11. List the actions required to execute a complete instruction ADD (R3), R1.
12. Draw and explain organization of the control unit to show conditional branching in the microprogram**.**